

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Marcos Karnezos	
Patent No.: 7,034,387 B2	Group Art Unit: 2814
Issued: 4/25/2006	Examiner: Chambliss, Alonzo
Title: SEMICONDUCTOR MULTIPACKAGE MODULE INCLUDING PROCESSOR AND MEMORY PACKAGE ASSEMBLIES	

U.S. Patent and Trademark Office
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

TRANSMITTAL FOR DECLARATION AND POWER OF ATTORNEY

Dear Sir:

An error was just discovered in the Declaration on file for this patent. The second page of the Assignment and the second page of the Declaration were apparently inadvertently interchanged at the time these documents were originally filed.

The correctly arranged Assignment is being separately submitted to the Assignment division for recordation.

Transmitted herewith is the correctly arranged Declaration titled "Combined Declaration and Power of Attorney for Utility Patent Application" for placement in the file.

Respectfully submitted,



Mikio Ishimaru
Reg. No. 27,449
October 22, 2008

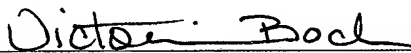
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CERTIFICATE OF MAILING/TRANSMISSION (37 C.F.R.1.8)

I hereby certify that this correspondence is on the date shown below being submitted to the United States Patent and Trademark Office via EFS-Web.

Date: October 22, 2008

Signature



Victoria Rocha